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Abstract

A signal processor for a joystick which comprises a joystick input device (11) which varies joystick voltage input value V_i according to an operating amount of a joystick (10) from a neutral position (11), an input means (13) which outputs the average value of the joystick voltage input value V_i read at every sampling time over a predetermined number of past occasions as a joystick voltage computation value V_{ic} , computation means (14) which computes an output computation value V_{oc} set according to the joystick voltage computation value V_{ic} , and operation start detecting means which detects an operation start when the joystick (10) is pushed over from the neutral position, are provided. The computation means (14) increases the output computation value V_{oc} to an effective maximum value when operation starts. In this way, over-sensitivity of control response when there is a sudden operation of the joystick 10 is suppressed.